TOSHIBA Field Effect Transistor Silicon N Channel Junction Type

## 2SK371

### For Low Noise Audio Amplifier Applications

Unit: mm

- Suitable for use as first stage for equalizer and MC head amplifiers.
- High  $|Y_{fs}|$ :  $|Y_{fs}|$  = 40 mS (typ.) (VDS = 10 V, VGS = 0, IDSS = 5 mA)
- High breakdown voltage:  $V_{GDS} = -40 \text{ V}$
- Super low noise: NF = 1.0dB (typ.)

 $(V_{DS} = 10 \text{ V}, I_{D} = 0.5 \text{ mA}, f = 1 \text{ kHz}, R_{G} = 100 \Omega)$ 

- High input impedance:  $I_{GSS} = -1 \text{ nA (max) (V}_{GS} = -30 \text{ V)}$
- Small package

### Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Gate-drain voltage	$V_{GDS}$	-40	V
Gate current	IG	10	mA
Drain power dissipation	P <sub>D</sub>	200	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

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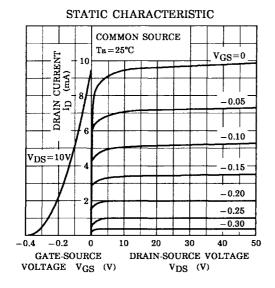
Weight: 0.13 g (typ.)

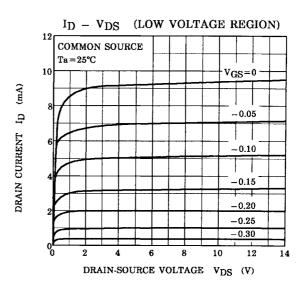
### **Electrical Characteristics (Ta = 25°C)**

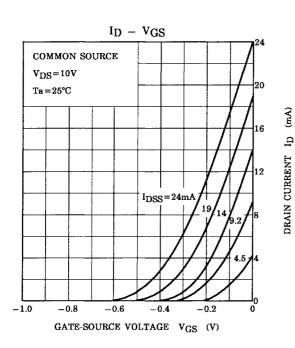
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate cut-off current	I <sub>GSS</sub>	$V_{GS} = -30 \text{ V}, V_{DS} = 0$	_	_	-1.0	nA
Gate-drain breakdown voltage	V (BR) GDS	$V_{DS} = 0$ , $I_G = -100 \mu A$	-40	_	_	V
Drain current	I <sub>DSS</sub> (Note 1)	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0	5.0	_	30	mA
Gate-source cut-off voltage	V <sub>GS</sub> (OFF)	$V_{DS} = 10 \text{ V}, I_D = 0.1 \mu A$	-0.3	_	-1.2	V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ kHz}, $ (typ.: $I_{DSS} = 5 \text{ mA}$ )	25	40		mS
Input capacitance	C <sub>iss</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	75	_	pF
Reverse transfer capacitance	C <sub>rss</sub>	V <sub>DG</sub> = 10 V, I <sub>D</sub> = 0, f = 1 MHz	_	15	_	pF
Noise figure (Note 2)	NF (1)	$V_{DS}$ = 10 V, $R_G$ = 100 $\Omega$ , $I_D$ = 5 mA, $f$ = 100 Hz	_	5	10	- dB
	NF (2)	$V_{DS}$ = 10 V, $R_G$ = 100 $\Omega$ , $I_D$ = 5 mA, $f$ = 1 kHz	_	1	2	

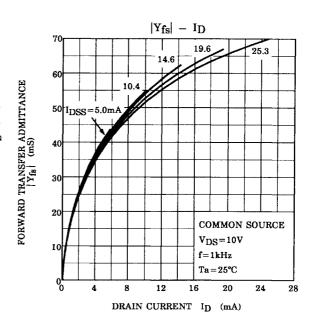
Note 1: I<sub>DSS</sub> classification GR: 5.0~10.0 mA, BL: 8.0~16.0 mA, V: 14.0~30.0 mA

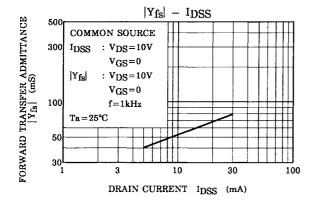
Note 2: Use this in the low voltage region ( $V_{DS}$  < 15 V) for low noise applications.

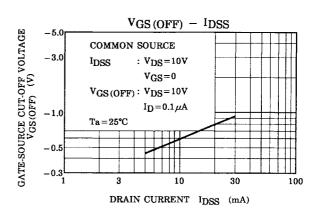




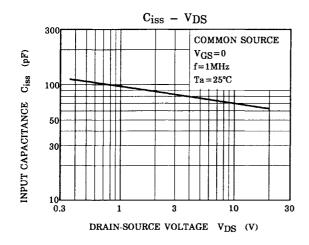


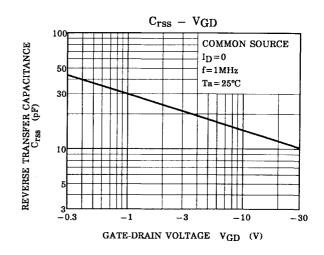


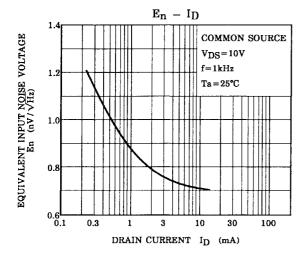


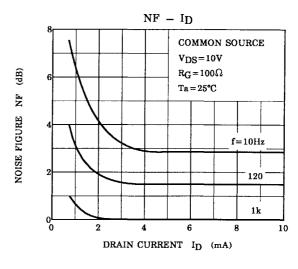


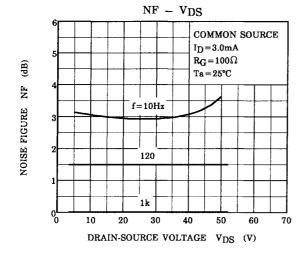
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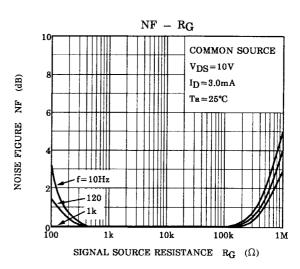


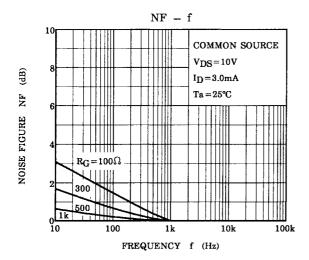


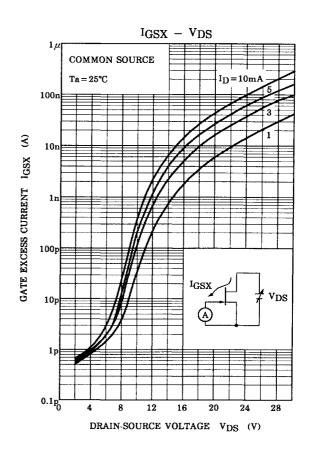


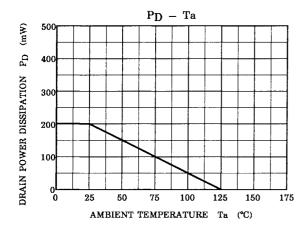












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